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PATENT

Practitioner's Docket No. KM1-003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Jono et al., Keiji

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Application No.: 10/007,300

Filed: 11/08/01

Group No.: 2811 Examiner: T.F. Tran

For: Trench-Isolated Transistors, Trench Isolation Structures, Memory Cells, and DRAMs (As Amended)

Commissioner for Patents Washington, D.C. 20231

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9306 on the date shown below:

Supplemental Information Disclosure Statement PTO-Form 1449 Cited Reference - 5,801,083 (Yu et al.)

Robin Saldivia

Signature

Total pages

(Certification of Facsimile Transmission--page 1 of 1)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No
Filing Date November 8, 2001
Inventor Keiji Jono et al.
Assignee Micron Technology, Inc.
Group Art Unit
Examiner T.F. Tran
Attorney's Docket No KM1-003
Title: Trench-Isolated Transistors, Trench Isolation Structures, Memory Cells, and
DRAMs (As Amended)

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Reference -See Attached Form PTO-1449

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449, a copy of which is attached. No admission is made regarding whether the submitted reference is prior art.

This Supplemental Information Disclosure Statement is being filed before the mailing of a first Office Action after the filing of a Request for Continued Examination (RCE), therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. §1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Citation of the referenced art is respectfully requested.

Respectfully submitted.

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